

EXPRESS MAIL LABEL NO: EV627626024US

Form PTO-1449

Applicant: Rudolf J. Hofmeister et al.

Serial No.: 10/626,965

Filing Date: July 25, 2003

For: OPTICAL AND ELECTRICAL CHANNEL FEEDBACK IN OPTICAL TRANSCEIVER  
MODULE



Sheet 1 of 2

Confirmation No.: 3965

Att'y Docket No.: 15436.215

Group: 2872

SUPPLEMENTAL INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>
DCP 1	5,557,437	09/17/1996	Sakai, e al.

**References Cited by Applicants**

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

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The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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Examiner:

/David C. Payne/

Date Considered:

07/26/2006

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Rudolf J. Hofmeister et al.

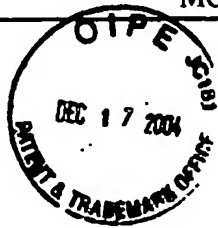
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DCP 1	5,854,704	12/29/1998	Grandpierre
DCP 2	6,256,127 B1	07/03/2001	Taylor
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DCP 5	2002/0060824 A1	05/23/2002	Liou et al.
DCP 6	2003/0053170 A1	03/20/2003	Levinson et al.

Foreign Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country or Patent Office</u>	<u>Translation</u>
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Other Documents

(including author, title, pertinent pages, etc.)

Examiner  
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DCP 7 *LXT16706/16707 SerDes Chipset*, Intel Products,  
www.intel.com/design/network/products/optical/phys/lxt16706.htm, April 19, 2002.

DCP 8 *LXT35401 XAUI-to-Quad 3.2G Transceiver*, Intel Products,  
www.intel.com/design/network/products/optical/phys/lxt35401.htm, April 19, 2002.

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- DCP 9 Texas Instruments User's Guide, *TLK2201 Serdes EVM Kit Setup and Usage*, Mixed Signal DSP Solutions, July 2000.
- DCP 10 Texas Instruments User's Guide, *TLK1501 Serdes EVM Kit Setup and Usage*, Mixed Signal Products, June 2000.
- DCP 11 National Semiconductor DS92LV16 Design Guide, *Serializing Made Simple*, February 2002.
- DCP 12 Vaishali Semiconductor, *Fibre Channel Transceiver*, VN16117, MDSN-0002-02, 08/09/2001.
- DCP 13 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*, January 1983.
- DCP 14 Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001, pp. 6-14.

#### References Cited by Applicants

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